Amendments to the Specification

Lines 3-15 of page 10 have been amended to read as follows:

The Schmitt trigger circuit includes a PMOS initialization transistor M10, an NMOS Schmitt trigger transistor M11, a disable transistor M12, an RC transistor M13, and a feedback transistor M14. The PMOS initialization transistor M10 is configured with a gate terminal coupled to the Schmitt trigger input node LB, a source terminal coupled to the supply signal VDD, and a drain terminal coupled to a reset signal node T1. The NMOS Schmitt trigger transistor M11 is configured with a gate terminal coupled to the Schmitt trigger input node LB, a ~~source~~ drain terminal coupled to the reset signal node T1, and a ~~drain~~ source terminal coupled to a threshold-enhancement node T3. The disable transistor M12 is configured with a gate terminal coupled to the Schmitt trigger input node LB, a ~~source~~ drain terminal coupled to the threshold-enhancement node T3, and a ~~drain~~ source terminal coupled to a ground potential. The feedback transistor M14 is configured with a gate terminal coupled to the reset signal node T1, a source terminal coupled to the threshold-enhancement node T3, and a drain terminal coupled to the supply signal VDD.